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L Number	Hits	Search Text	DB	Time stamp
1	112	((ROM (Read adj only adj memory))same(RAM (random adj access adj memory))) same (Microcontroller))same ((routine (main adj (routine program application))near10 (subroutine))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/16 12:15
2	0	709/\$.ccls. and ((ROM (Read adj only adj memory))same(RAM (random adj access adj memory))) same (Microcontroller))same ((routine (main adj (routine program application))near10 (subroutine))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/16 12:16
3	0	718/\$.ccls. and ((ROM (Read adj only adj memory))same(RAM (random adj access adj memory))) same (Microcontroller))same ((routine (main adj (routine program application))near10 (subroutine))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/16 12:16
-	5119	network adj interface adj card	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/12/18 08:18
-	2345	(network adj interface adj card) and @ad<20001115	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/12/18 13:45
-	615	((network adj interface adj card) and @ad<20001115) and ((routine\$2 subroutine\$3) and (RAM ROM Vector\$3))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/12/17 16:37
-	70	((network adj interface adj card) and @ad<20001115) and microcontroller) and ((routine\$2 subroutine\$3) and (RAM ROM Vector\$3))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/12/17 14:11
-	10	((network adj interface adj card) and @ad<20001115) and microcontroller) and ((routine\$2 and subroutine\$3) and (RAM ROM Vector\$3))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/12/17 15:11
-	33	((network adj interface adj card) and @ad<20001115) and ((routine\$2 subroutine\$3) and (RAM ROM Vector\$3))) and (multitask\$4 and control\$4)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/12/17 14:49
-	2	((network adj interface adj card) and @ad<20001115) and ((routine\$2 subroutine\$3) and (RAM ROM Vector\$3))) and (multitask\$4 and control\$4)) and ((routine\$2 subroutine\$3) and (RAM ROM Vector\$3))) and microcontroller	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/12/17 14:51
-	33	((network adj interface adj card) and @ad<20001115) and ((routine\$2 subroutine\$3) and (RAM ROM Vector\$3))) and (multitask\$4 and control\$4)) and ((routine\$2 subroutine\$3) and (RAM ROM Vector\$3))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/12/17 14:55
-	596	physical adj layer adj interface	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/12/17 15:45
-	360	(physical adj layer adj interface) and @ad<20001115	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/12/17 16:44
-	0	((physical adj layer adj interface) and @ad<20001115) and ((routine\$2 and subroutine\$3) and (RAM ROM Vector\$3))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/12/17 15:11

-	44	((physical adj layer adj interface) and @ad<20001115) and ((routine\$2 subroutine\$3) and (RAM ROM Vector\$3))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/12/17 15:11
-	9	((physical adj layer adj interface) and @ad<20001115) and ((routine\$2 subroutine\$3) and (RAM ROM Vector\$3))) and microcontroller	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/12/17 15:34
-	295	((physical adj layer adj interface) and @ad<20001115) and ((Control\$4 monitor\$4)and (function state status))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/12/17 15:23
-	17	((physical adj layer adj interface) and @ad<20001115) and ((Control\$4 monitor\$4)and (function state status))) and microcontroller	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/12/17 15:35
-	0	((network adj interface adj card) and @ad<20001115) and microcontroller) and (physical adj layer adj interface)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/12/17 15:45
-	14	((network adj interface adj card) and @ad<20001115) and microcontroller) and ((upgrad\$4 patch\$3 reprogram\$5) and (state adj machine))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/12/17 16:03
-	874	712/247,245,243,233.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/12/17 16:03
-	798	712/247,245,243,233.ccls. and @ad<20001115	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/12/17 16:04
-	3	(712/247,245,243,233.ccls. and @ad<20001115) and (network adj interface adj card)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/12/17 16:04
-	0	((network adj interface adj card) and @ad<20001115) and microcontroller) and (Multi\$4 adj (task routine) adj (control)adj (program))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/12/17 16:07
-	2	(Multi\$4 adj (task routine) adj (control)adj (program))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/12/17 16:13
-	4	(Multitasking) adj (control)adj (program)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/12/17 16:13
-	163	((network adj interface adj card) and @ad<20001115) and microcontroller	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/12/17 16:16
-	1602	Programmable and (network adj (interface\$3 adapter\$3)adj card)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/12/17 16:17

-	5	Programmable near2 (network adj (interface\$3 adapter\$3)adj card)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/12/17 16:17
-	2	(((network adj interface adj card) and @ad<20001115) and ((routine\$2 subroutine\$3) and (RAM ROM Vector\$3))) and ((Patch\$2 reprogram repair)near3(routine\$2 subroutine\$3))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/12/17 16:41
-	254	((Patch\$2 reprogram repair)adj(routine\$2 subroutine\$3))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/12/17 16:41
-	1	(((Patch\$2 reprogram repair)adj(routine\$2 subroutine\$3))) and (network adj interface adj card)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/16 09:39
-	3491	((network adj interface adj card) NIC "NIC") and (controller)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/12/17 16:44
-	1804	(((network adj interface adj card) NIC "NIC") and (controller)) and @ad<20001115	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/12/18 08:16
-	560	Embedded adj microcontroller	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/12/18 13:45
-	1	((Embedded adj microcontroller) and @ad<20001115) and network adj interface adj card	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/12/18 08:18
-	31	((Embedded adj microcontroller) and @ad<20001115) and (call\$3 near (routine subroutine function))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/12/18 13:46
-	37	((Embedded adj microcontroller) and @ad<20001115) and (call\$3 near3 (routine subroutine function))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/12/18 13:43
-	108035	324/\$.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/12/22 10:32
-	12	Sun-albert-c.in.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/12/18 12:30
-	2	4530051.pn.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/12/18 13:44
-	0	in-circuit adj program\$%	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/12/18 13:45

-	115	in-circuit adj program\$4	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/12/18 13:45
-	78	((in-circuit adj program\$4) and @ad<20001115	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/12/22 08:25
-	3	((((in-circuit adj program\$4) and @ad<20001115) and microcontroller) and ((Embedded adj microcontroller) and @ad<20001115) and (call\$3 near (routine subroutine)))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/12/18 13:46
-	3	((((in-circuit adj program\$4) and @ad<20001115) and microcontroller) and ((Embedded adj microcontroller) and @ad<20001115)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/12/18 13:49
-	27	((in-circuit adj program\$4) and @ad<20001115) and microcontroller	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/12/18 13:49
-	2345	((network adj interface adj card) and @ad<20001115	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/12/18 14:15
-	0	((network adj interface adj card) and @ad<20001115) and ((Multitask\$4) Near10 ((control)adj (program)))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/12/18 14:16
-	114	((network adj interface adj card) and @ad<20001115) and((control)adj (program))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/12/18 14:17
-	23	((((network adj interface adj card) and @ad<20001115) and((control)adj (program))) and ((call\$3 invoke invoca\$5)near10(routine subroutine)))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/12/22 08:26
-	286	((Embedded adj microcontroller) and @ad<20001115	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/16 10:43
-	173	((in-system adj program\$4) and @ad<20001115	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/12/22 08:26
-	0	((((in-system adj program\$4) and @ad<20001115) and (((network adj interface adj card)and((control)adj (program))) and ((call\$3 invoke invoca\$5)near10(routine subroutine))))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/16 10:56
-	2	((((in-system adj program\$4) and @ad<20001115) and (((control)adj (program))) and ((call\$3 invoke invoca\$5)near10(routine subroutine))))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/12/22 08:33
-	1	(((((in-system adj program\$4) and @ad<20001115) and (((control)adj (program))) and ((call\$3 invoke invoca\$5)near10(routine subroutine)))) and 711/\$.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/12/22 08:33

-	8	((in-system adj program\$4) and @ad<20001115) and 711/\$.cccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/12/22 08:34
-	108035	324/\$.cccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/12/22 11:04
-	135	324/376,377.cccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/12/22 11:06
-	294	324/415,416.cccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/12/22 12:13
-	2	6182238.pn.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/12/22 12:14
-	2	4530051.pn.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/12/22 12:14
-	3785	((ROM (Read adj only adj memory))and (RAM (random adj access adj memory))) and (Microcontroller)) and ((routine (main adj (routine program application)) same (metaroutine subroutine subprogram))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/16 10:44
-	3152	((ROM (Read adj only adj memory))same(RAM (random adj access adj memory))) and (Microcontroller)) and ((routine (main adj (routine program application)) same (metaroutine subroutine subprogram))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/16 10:32
-	1468	((ROM (Read adj only adj memory))same(RAM (random adj access adj memory))) same(Microcontroller)) and ((routine (main adj (routine program application)) same (metaroutine subroutine subprogram))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/16 09:45
-	112	((ROM (Read adj only adj memory))same(RAM (random adj access adj memory))) same(Microcontroller)) same((routine (main adj (routine program application)) same (metaroutine subroutine subprogram))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/16 09:45
-	3782	((ROM (Read adj only adj memory))and (RAM (random adj access adj memory))) and (Microcontroller)) and ((routine (main adj (routine program application)) same (subroutine))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/16 10:37
-	112	((ROM (Read adj only adj memory))same(RAM (random adj access adj memory))) same (Microcontroller))same ((routine (main adj (routine program application)) same (subroutine))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/16 10:41
-	112	((ROM (Read adj only adj memory))same(RAM (random adj access adj memory))) same (Microcontroller))same ((routine (main adj (routine))near10 (subroutine))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/16 09:51
-	112	((ROM (Read adj only adj memory))same(RAM (random adj access adj memory))) same (Microcontroller))same ((routine (main adj (routine))and (subroutine))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/16 09:52

-	112	((ROM (Read adj only adj memory))same(RAM (random adj access adj memory))) same (Microcontroller))same ((routine (main adj (routine))and (sub-routine)))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/16 09:52
-	0	((((ROM (Read adj only adj memory))same(RAM (random adj access adj memory))) and (Microcontroller)) and ((routine (main adj (routine program application)) same (sub adj routine))) and (NIC (network adj interface adj card)) and (call\$5 order\$5 instruct\$5 provid\$5) near10 (sub adj routine) and (reset\$5 updat\$5) near10 (vector)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/16 10:38
-	0	(NIC (network adj interface adj card)) and ((call\$5 order\$5 instruct\$5 provid\$5) near10 (sub adj routine) same (reset\$5 updat\$5) near10 (vector))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/16 10:48
-	1	(Microcontroller) and ((call\$5 order\$5 instruct\$5 provid\$5) near10 (sub adj routine) same (reset\$5 updat\$5) near10 (vector))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/16 10:40
-	112	((ROM (Read adj only adj memory))same(RAM (random adj access adj memory))) same (Microcontroller))same ((routine (main adj (routine program application)) same (sub adj routine)))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/16 10:45
-	4	(ROM)same(RAM) same (Microcontroller)same ((routine) same (sub adj routine))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/16 10:43
-	530	Embedded adj microcontroller	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/16 10:44
-	81	(Embedded adj microcontroller) and ((ROM (Read adj only adj memory))and (RAM (random adj access adj memory))) and (Microcontroller)) and ((routine (main adj (routine program application)) same (sub adj routine)))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/16 10:44
-	81	(Embedded adj microcontroller) and ((ROM (Read adj only adj memory))and (RAM (random adj access adj memory))) and ((routine (main adj (routine program application)) same (sub adj routine)))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/16 10:44
-	151	(Embedded adj microcontroller) and(routine (main adj (routine)) same (sub adj routine))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/16 10:47
-	0	(Embedded adj microcontroller) and (main adj (routine)) same (sub adj routine)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/16 10:48
-	0	((main adj (routine)) same (sub adj routine)) and (call\$5 order\$5 instruct\$5 provid\$5) near10 (sub adj routine) same (reset\$5 updat\$5) near10 (vector)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/16 10:49
-	460	(main adj (routine)) same (sub adj routine)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/16 10:52

-	2	((ROM) same (RAM) same (Microcontroller)) and (main adj (routine)) same (sub adj routine)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/16 10:50
-	0	(multitask\$5 near5 vector) and (main adj (routine)) same (sub adj routine)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/16 10:53
-	1	((updat\$5 refresh\$5 reset\$5) near5 vector) and (main adj (routine)) same (sub adj routine)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/16 10:55
-	1	((updat\$5) near5 vector) and (main adj (routine)) same (sub adj routine)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/16 10:55
-	0	(in-system adj program\$4) and (network adj interface adj card) and (control) adj (program) and ((call\$3 invoke invoca\$5) near10 (routine subroutine))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/16 10:58
-	54	(network adj interface adj card) and (control) adj (program) and ((call\$3 invoke invoca\$5) near10 (routine subroutine))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/16 10:58
-	0	(network adj interface adj card) and (control) adj (program) same ((call\$3 invoke invoca\$5) near10 (routine subroutine))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/16 10:58

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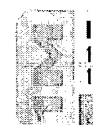
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Relevance scale ☐ ☐ ☐ ☐ ☐**1 Model Refinement for Hardware-Software Codesign**

Jie Gong, Daniel D. Gajski, Smita Bakshi

March 1996 **Proceedings of the 1996 European conference on Design and Test**

Full text available: pdf(663.82 KB)

Additional Information: [full citation](#), [abstract](#)[Publisher Site](#)

The model refinement task in system-level synthesis transforms a specification from a functional model to a chosen implementation model. In this paper, we categorize several commonly-used implementation models and then describe a set of refinement procedures to transform a specification to each of these implementation models. We also present a set of experimental results to compare the implementation models and to demonstrate how the proposed approach is used to explore different implementation ...

Keywords: model refinement, system-level synthesis, hardware/software codesign, interface models, communication protocols

2 The automated generation of cross-system software for supporting micro/mini computer systems

Gearold R. Johnson, Robert A. Mueller

 March 1976 **ACM SIGPLAN Notices , Proceedings of the ACM SIGMINI/SIGPLAN interface meeting on Programming systems in the small processor environment**, Volume 11 Issue 4

Full text available: pdf(783.32 KB)

Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

ASM/GEN and SIM/GEN are a software system comprised of a set of independent FORTRAN program writer modules designed to generate micro computer and small minicomputer assemblers and simulators. It is simple enough to be used by those with limited architecture and programming backgrounds, but flexible and powerful enough to generate efficient and well-structured assemblers and simulators for small micro/mini computers with sophisticated architectures and instruction sets. This paper presents ...

3 DATAPATH: a CMOS data path silicon assembler

Tom Marshburn, Ivy Lui, Rick Brown, Dan Cheung, Gary Lum, Peter Cheng

July 1986 **Proceedings of the 23rd ACM/IEEE conference on Design automation**

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Full text available:  pdf(898.11 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

As an integration of automatic silicon assembly and simulation tools, the DATAPATH Silicon Assembler produces mask geometries and netlists from input specifications written in a Hardware Description Language, MADL. DATAPATH consists of a library of data path cells (i.e. registers, bus prechargers, drivers, interconnects, ALU's and other logic elements) in a flexible bus architecture. The cells are highly parameterized and procedurally described in a hierarchical manner. The layout is automa ...

4 [Modularity and multi-microprocessor structures](#)

D. P. Siewiorek

September 1974 **Conference record of the 7th annual workshop on Microprogramming**

Full text available:  pdf(620.29 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

There are significant advantages to a modular approach to system design. As semiconductor technology has evolved the complexity of what a designer considers as a design primitive has also grown. Systems constructed from the next generation of module sets will bear a strong resemblance to multiprocessor structures. A taxonomy of the multiprocessor space is given and the subspace to be occupied by the next generation module sets is delineated. Summaries are given of recent research results on ...

5 [Formally based static analysis of microcode](#)

J. M. Foster

December 1986 **ACM SIGMICRO Newsletter , Proceedings of the 19th annual workshop on Microprogramming**, Volume 17 Issue 4

Full text available:  pdf(822.75 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

Algebraic methods have been widely used to find properties of programs, especially for use in compiler optimisation. This paper describes the use of this kind of method to prove the absence of particular errors in microcode, or to detect and locate such errors. In order to show the kind of error which may be found we consider a number of examples. All of these have found errors in practical microcode, written for the PERQ computer.

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